

USB4 1.0 ENGINEERING CHANGE NOTICE FORM

Title: Adding Disable Time Sync (DTS) Bit to TMU_ADP_CS_6
Applied to: USB4 Specification Version 1.0

Brief description of the functional changes:

Adds a bit that allows the CM to disable Time Sync handshakes on Adapter level
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Benefits as a result of the changes:

New required bit in TMU_ADP_CS_6

An assessment of the impact to the existing revision and systems that currently conform to the USB specification:
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None

An analysis of the hardware implications:
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Adding a bit that disables TMU Handshakes

An analysis of the software implications:
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None

An analysis of the compliance testing implications:
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None

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Actual Change

(a). Section 8.2.2.2, Table 8-11 Page 295

From Text:

DW	Register Name	Bit(s)	Field Name and Description	Type	Default Value
6	TMU_ADP_CS_6	15:0	Reserved	RsvdZ	0
		31:16	Vendor Defined	VD	Vendor Defined

To Text:

DW	Register Name	Bit(s)	Field Name and Description	Type	Default Value
6	TMU_ADP_CS_6	0	Reserved	RsvdZ	0
		<u>1</u>	<u>Disable Time Sync (DTS)</u> <u>A Connection Manager uses this bit to disable Time Synchronization Handshake initiation.</u> <u>If set to 1, the Adapter shall not send any Delay Requests or Delay Responses.</u> <u>If set to 0b, the Adapter may send Delay Requests or Delay Responses.</u>	<u>R/W</u>	<u>0</u>
		15:2	Reserved	RsvdZ	0
		31:16	Vendor Defined	VD	Vendor Defined

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(b). Section 8.2.2.2, Figure 8-8 Page 293

From Text:

Figure 8-8. Structure of the TMU Adapter Configuration Capability

31										0													
Reserved										Capability ID					Next Capability Pointer					0			
TxTimeToWire																				1			
RxTimeToWire																				2			
I	I	U	Reserved																	3			
D	D	D	TX TSNOS counter										RX TSNOS counter										4
S	M	M	TX Packet counter										RX Packet counter										5
Reserved															D	D	D	R	6				
Rsvd		Bad Packet Counter					Lost Packet Counter					Lost TSNOS Counter					7						

To Text:

Figure 8-8. Structure of the TMU Adapter Configuration Capability

31										0														
Reserved										Capability ID					Next Capability Pointer					0				
TxTimeToWire																				1				
RxTimeToWire																				2				
I	I	U	Reserved																	3				
D	D	D	TX TSNOS counter										RX TSNOS counter										4	
S	M	M	TX Packet counter										RX Packet counter										5	
Vendor Defined															Reserved					D	T	S	R	6
Rsvd		Bad Packet Counter					Lost Packet Counter					Lost TSNOS Counter					7							